

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7101	345/87,94,98-100,211.ccls.	US-PGPUB; USPAT	OR	OFF	2007/07/30 13:16
L2	3	((dynamic adj2 register) with clock\$3 with (level adj shift\$3))	US-PGPUB; USPAT	OR	ON	2007/07/30 13:19
L3	1	1 and 2	US-PGPUB; USPAT	OR	ON	2007/07/30 13:17
L4	2	2 not 3	US-PGPUB; USPAT	OR	ON	2007/07/30 13:18
L5	1204	(dynamic adj2 register)	US-PGPUB; USPAT	OR	ON	2007/07/30 13:19
L7	14878	(level adj shifter) or (level adj2 shift\$3 adj2 circuit\$2)	US-PGPUB; USPAT	OR	ON	2007/07/30 13:25
L8	13069	target\$3 adj2 level	US-PGPUB; USPAT	OR	ON	2007/07/30 13:21
L9	625	relay\$3 adj2 level	US-PGPUB; USPAT	OR	ON	2007/07/30 13:21
L10	1	1 and 5 and 7 and 8 and 9	US-PGPUB; USPAT	OR	OFF	2007/07/30 13:24
L11	1	1 and register and 7 and 8 and 9	US-PGPUB; USPAT	OR	ON	2007/07/30 13:22
L13	2	1 and 5 and 7	US-PGPUB; USPAT	OR	ON	2007/07/30 13:24
L14	1	5 and 7 and 8 and 9	US-PGPUB; USPAT	OR	ON	2007/07/30 13:24
L15	1	7 and 8 and 9	US-PGPUB; USPAT	OR	ON	2007/07/30 13:25
L17	0	((level adj shifter) or (level adj2 shift\$3 adj2 circuit\$2)) and (relay\$3 adj2 level) and (target\$3 adj2 level) and (clock\$3 adj2 signal)).ab.	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 13:27
L18	1	((level adj shifter) or (level adj2 shift\$3 adj2 circuit\$2)) and (relay\$3 adj2 level) and (target\$3 adj2 level) and (clock\$3 adj2 signal)).clm.	US-PGPUB; USPAT	OR	ON	2007/07/30 14:14
L19	8	((level adj shifter) or (level adj2 shift\$3 adj2 circuit\$2)) and (amplif\$4 near9 (clock\$3 adj2 signal)) and register).clm.	US-PGPUB; USPAT	OR	ON	2007/07/30 14:15